

Class in charge: Sri. N.SRIKANTH

Room No: BIV 003

W.E.F : 09-09-2019

Day/Time	09.40 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 01.00	01.00 - 02.00	02.00 - 02.50	02.50 - 03.40	03.40 - 04.30
Monday	RRS	SD	NSM	CEA	L U N C H	CED LAB		
Tuesday	LARCS	SEMINAR				SD	ACT	CPM
Wednesday	ACT	LARCS	RRS	CPM		SD	NSM	Library
Thursday	CPM	LARCS	ACT	RRS		NSM	CPM	RRS
Friday	LARCS	NSM	SD	ACT		ACT LAB		
Saturday								

S.No	COURSE CODE	COURSE NAME	FACULTY
1	NSM	: Numerical and Statistical Methods	Sri N.Raji reddy
2	LARCS	: Limit Analysis of Reinforced Concrete Structures	Smt. K.S.Bhargavi
3	CPM	: Construction Planning and Management	Sri K.R Pranav
4	SD	: Structural Dynamics	Sri.N.Srikanth
5	R&RS	: Repair and Rehabilitation of Structures	Ms.K.Manasa
6	ACT	: Advanced Concrete Technology	Sri.K.Srujan Varma
7	ACT Lab	: Advanced Concrete Technology Lab	Ms.K.Manasa
8	CAD Lab	: Computer Aided Design Laboratory	Sri.S.Raja shekar

Bo
I/c Time Tables
A. BHASKAR/P.T. ABHILASH

M
Co-Ordinator Time Tables
Dr.M. ANDAL

S.R.D.
Head, Dept. of Civil Engg.,
Prof. L.SUDHEER REDDY

C. Venkatesh
Dean, Academic Affairs
Dr. C. VENKATESH

K.A.R.
Principal.
Dr K.ASHOKA REDDY



KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE: WARANGAL-15

(An Autonomous Institute under Kakatiya University, Warangal)

DEPARTMENT OF MECHANICAL ENGINEERING

M.TECH. (DESIGN ENGINEERING) I - SEMESTER

Class Work Time-Table for the Academic Year 2019 - 20

Class Teacher: Sri. G. Srinu

Class Room: B-IV 215, 213, B-III 215

W.E.F: 03.09.2019

Day/Time	9.40 10.30	10.30 11.20	11.20 12.10	12.10 1.00	1.00 2.00	2.00 2.50	2.50 3.40	3.40 4.30
Monday	MV B-IV 213	OTED B-IV 213	OTED B-IV 213	ELECTIVE-I B-IV 213	L U N C H	ELECTIVE-II B-IV 215	CAED B-IV 215	CAED B-IV 215
Tuesday	ELECTIVE-II B-III 215	SA B-III 215	SA B-III 215	RC B-III 215		MV B-IV 215	OTED B-IV 215	ELECTIVE-I B-IV 215
Wednesday	ELECTIVE-II B-IV 228	SEMINAR				CAED B-IV 215	SA B-IV 215	MV B-IV 215
Thursday	MV B-III 215	MECHANICAL VIBRATIONS LAB				OTED B-IV 215	ELECTIVE-I B-IV 215	RC B-IV 215
Friday	ELECTIVE - I B-IV 005	CAD LAB				SA B-IV 213	CAED B-IV 213	ELECTIVE-II B-IV 213
Saturday	SEMINAR			-----				

Optimization Techniques in Engg Design (OTED) : Sri. K. Venu Madhav
 Stress Analysis (SA) : Sri. P. Rajesh
 Mechanical Vibrations (MV) : Ms. A. KeerthiChandra
 Computer Aided Engineering & Design (CAE&D) : Sri. G. Srinu
 ELECTIVE - I - Principles of Product Design : Sri. D. Sammaiah
 ELECTIVE - II - Advanced Fluid Mechanics : Dr. E. Ramesh
 CAD Lab : Sri. V. Rakesh & Ms. G. Sumithra
 Mechanical Vibrations Lab : Ms. A. KeerthiChandra & Ms. P. Divya
 SEMINAR : DPGRC - Dept. Post Graduate Review Committee

ALMANAC	
COMMENCEMENT OF CLASSWORK	03.09.2019
Class work duration (Phase - I)	09.09.2019 to 26.10.2019
VACATION	07.10.2019 to 13.10.2019
Mid Semester Examination - I (MSE-I)	28.10.2019 to 02.11.2019
Class work duration (Phase - II)	04.11.2019 to 14.12.2019
LAST DAY OF INSTRUCTION	14.12.2019
Mid Semester Examination - II (MSE-II)	16.12.2019 to 21.12.2019
Commencement of End Semester Examination(ESE) (Theory & Practicals)	23.12.2019
COMMENCEMENT OF CLASSWORK (Commencement of Semester - II Class work)	20.01.2020

I/C Time Tables

Sri. A. HARI KUMAR
Sri. MOHD. SAMEER

I/C Overall Time Tables
Dr. M. ANDAL

Head, MED
Prof. K. SRIDHAR

Dean, Academics
Prof. C. VENKATESH

Principal
Prof. K. ASHOKA REDDY

KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE (AN AUTONOMOUS INSTITUTE): WARANGAL - 15
 DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
 M.Tech Digital Communication I-Semester Class Work Tinte Table (2019-20)

Class Teacher: B.Komuraiah

Room. No. B-VI-201, 202, 203

w.e.f. 09.09.2019

Day / Time	9.40 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00		2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
Monday	-	DCT B-VI-202	MWOFCS B-VI-202	MDCT B-VI-202	L	ADSP B-VI-201	DET B-VI-201	ANN B-VI-201
Tuesday	-	-	DET B-VI-201	ADSP B-VI-201		U	DCT B-VI-202	MDCT B-VI-202
Wednesday	-	ADSP B-VI-201	DCT B-VI-201	MWOFCS B-VI-201	N	←----ADSP LAB----→ RS/BK		
Thursday	DCT B-VI-203	MDCT B-VI-203	ADSP B-VI-203	MWOFCS B-VI-203	C	DET B-VI-202	ANN B-VI-202	-
Friday	←----MWOFCS LAB----→ VVR/DSR			DET B-VI-206	H	MDCT B-VI-203	ANN B-VI-203	MWOFCS B-VI-203
Saturday	←-----T&P/CCA/JKC-----→						-	-

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|------------------|---|---------------------------------|
| DCT | : Data Compression Techniques | : B.Komuraiah (BK) |
| MDCT | : Modern Digital Communication Techniques | : Dr. A.Subbarao(AS) |
| MW&OFCS | : Microwave & Optical Fiber Communication Systems | : Dr.V.Venkateswar Reddy(VVR) |
| ADSP | : Advanced Digital Signal Processing | : R.Srikanth(RS) |
| ANN | : Artificial Neural Networks | : Syed Zaheeruddin (SYZ) |
| DET | : Detection & Estimation Theory | : Dr.M.Raju(MR) |
| MW&OFCS Lab | : Microwave & Optical Fiber Communication Systems lab | : VVR/D.Srinivas Rao (DSR) |
| ADSP Lab | : Advanced Digital Signal Processing lab | : RS/BK |
| ECEA | : ECE Association Activity | : B.Komuraiah/Syed Zaheeruddin |
| | | : /Dr.K.Sowjanya/J.Nandini |
| T&P / CCA / TASK | : Training & Placement /Cultural & Co-curricular Activity/
Telangana Association for Skill and Knowledge | : A.Srinivas/ K.Ramudu/ V. Raju |


I/c Time Table


Coordinator
(Time Tables)


Head, Dept of ECE


Dean, Academic Affairs


PRINCIPAL

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE: WARANGAL
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
CLASS WORK TIME TABLE FOR THE ACADEMIC YEAR - 2019-2020
M.Tech. (Software Engineering) I Year I Semester

Room No.

W.e.f: 03.09.2019

Day/Time	9:40 to 10:30	10:30 to 11:20	11:20 to 12:10	12:10 to 1:00	1:00 to 2:00	2.00 to 2.50	2.50 to 3.40	3.40 to 4.30
Monday	HCI Graphics Lab	SRE B V 204	DMOT B V 204	OOSE B V 204	L U N C H	ADS B V 202	DMOT B V 202	Remedial Class B V 202
Tuesday	OOSE LAB+SVM+PR-SD Lab			SOA B V 304		HCI B V 302	DMOT B V 302	Remedial Class B V 302
Wednesday	SRE Graphics Lab	ADS B IV 231	OOSE B IV 231	SOA B IV 231		OOSE B V 204	HCI B V 204	ADS BV 204
Thursday	SOA Graphics Lab	OOSE Graphics Lab	SRE Graphics Lab	HCI Graphics Lab		Adv. S/W Lab+BRR+BS- SD Lab		
Friday	SOA B V302	SRE B V302	DMOT B V302	ADS B V302				
Saturday	-----Seminars-----							

DMOT Discrete Mathematics & Optimization Techniques
OOSE Object Oriented Software Engineering
SRE Software Requirements & Estimation
ADS Advanced Data Structures and Algorithms
E1 Elective-I : Software Oriented Architecture
E2 Elective-II: Human Computer Interaction

Dr. R.Ramesh
S.Venkatramulu
S.Sravanthi
B.Srinivas
M.S.B Prudviraj
S.Nagaraju

OOSE Lab:
Advanced Software lab:
Seminar:

S.Venkatramulu(SVM) +PR
B.Raghuram (BRR)+ BS
DPGRC

I/c. Time-Tables



Co-Ordinator
Time-Tables



Head of the Department



Dean, Academic Affairs



Principal



KAKATIYA INSTITUTE OF TECHNOLOGY AND SCIENCE: WARANGAL - 15
DEPARTMENT OF ELECTRONICS & INSTRUMENTATION ENGINEERING
M.TECH (VLSI & EMBEDDED SYSTEM) (I-Semester) Class work Time Table (2019-20)

Class Teacher: Sri O.Anjaneyulu


Room No. BIV- 001 A

w.e.f. 09.09.2019

Day	9.40 - 10.30	10.30 -11.20	11.20 -12.10	12.10 - 1.00	L U N C H	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
Mon	VLSI	DD	DCCN	DMOT		ESD	HDL	←--Lab Duty--→
Tue	HDL	DCCN	DD	ESD		←----VT Lab--→ OA		
Wed	DCCN	DMOT	DD	HDL		VLSI	ESD	←--Lab Duty--→
Thu	VLSI	DD	DMOT	EIEA		HDL	DCCN	←--Lab Duty--→
Fri	ESD	←--DD Lab---→ BJ				DMOT	VLSI	←--Lab Duty--→
Sat								

DD	: Digital Design	:	Dr.K.Sivani (KS)
VLSI	: VLSI Technology	:	Sri O.Anjaneyulu (OA)
DMOT	: Discrete Mathematics & Optimization Techniques	:	Dr.D.Rajaiah (DR)
ESD	: Embedded System Design	:	Sri B.Krishna Sundeep (BKS)
HDL	: Hardware Description Languages	:	Dr.K.Srinivas (KSC)
DCCN	: Data Communication & Computer Networks	:	Sri B.Venu Maheswar (BVM)
VT LAB	: VLSI Technology Laboratory	:	Sri O.Anjaneyulu (OA)
DD LAB	: Digital Design Laboratory	:	Sri B.Jeevan(BJ)
EIEA	: Electronics & Instrumentation Engineering Association		


I/c Time Table
(Smt B.Smitha)


Coordinator
(Time Tables)
Dr.M.Andal


HOD, Dept. of E&I
(Dr.K.Venu Madhav)


Dean,
Academic Affairs
(Dr.C.Venkatesh)


PRINCIPAL
(Dr.K.Ashoka Reddy)

KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL-15
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
Classwork Timetable for the year 2019-2020
I SEMESTER of M.Tech. (POWER ELECTRONICS)

IPE

w.e.f. 09.09.2019
Class Room: B-IV 001B

Class Teacher: Dr. A. MADHUKAR RAO

DAY\TIME	9.40-10.30	10.30-11.20	11.20-12.10	12.10-13.00	LUNCH	14.00-14.50	14.50-15.40	15.40-16.30
MONDAY	DSP	PECDC	HVDC	MCT		APEC	ASEE	LIBRARY
TUESDAY	PECDC	PES LAB				MCT	APEC	ASEE
WEDNESDAY	APEC	MCT	HVDC	EEEE AUDITORIUM		ASEE	PECDC	DSP
THURSDAY	HVDC	APEC	ASEE	DSP		PE LAB		
FRIDAY	HVDC	MCT	PECDC	DSP		SEM		
SATURDAY	SEM							

HVDC : High Voltage DC Transmission
MCT : Modern Control Theory
APEC : Advanced Power Electronic Converters
DSP : Digital Signal Processor
ASEE : Alternate sources of Electrical Energy
PECDC : Power Electronic Control of DC drives
PE LAB : Power Electronics Laboratory
PES LAB : Power Electronic Simulation Laboratory
SEM : Seminar
EEEE : Electrical & Electronics Engineering Association

Sri P. Nagarjuna Reddy
Dr A. Rajasekhar
Sri C. Pavan Kumar
Dr. A. Madhukar Rao
Sri K. Srinivas
Sri V. Srinivas
Dr. A. Madhukar Rao, Sri K. Srinivas
Smt M. Spandana, Ms. B. Reshma
Dr. A. Madhukar Rao


J. Nagaraj
In-charge, Time Table


Coordinator, Time Tables


Head of the Department


Dean, Academic Affairs


Principal